

**In the Specification:**

Please insert at Page 1, line 2, the following paragraph:

**RELATED PATENT APPLICATION**

This application is a National Phase Application of PCT/IL03/00695 having International Filing Date of 21 August 2003, which claims priority from U.S. Provisional Patent Application No. 60/404,918 filed 22 August 2002.

Please amend the paragraph beginning on Page 6, line 28, as follows:

Figs. 9 and 9a – 9f show internal wiring of PCB layers for the segments for use in a display device according to a preferred embodiment of the present invention; and